

FIGURE 1

FIGURE 2

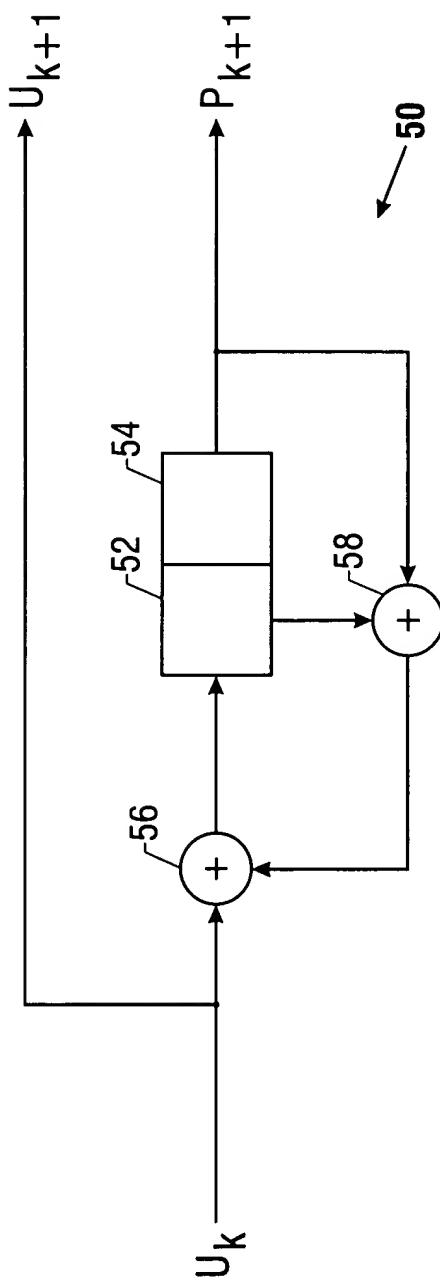


FIGURE 3

state transition table <u>60</u>	state k (shift regs)	U _k input	code word		state k+1 (shift regs)
			U _{k+1}	P _{k+1}	
	00	0	00	00	00
	00	1	10	10	10
	01	0	00	00	10
	01	1	10	00	00
	10	0	01	11	11
	10	1	11	01	01
	11	0	01	01	01
	11	1	11	11	11

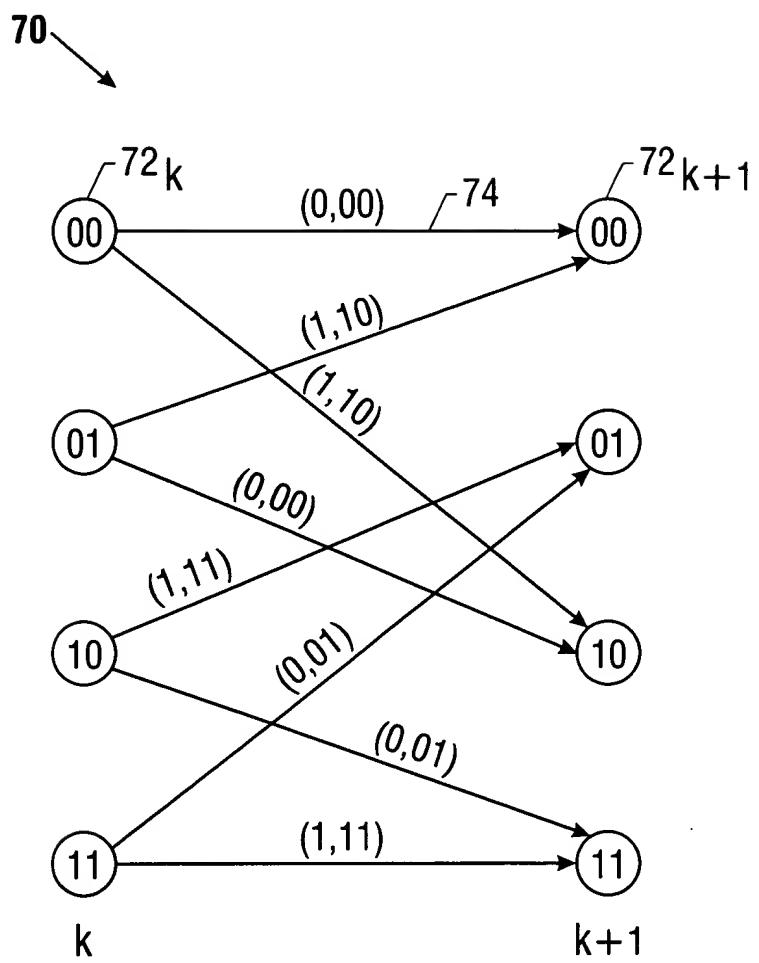
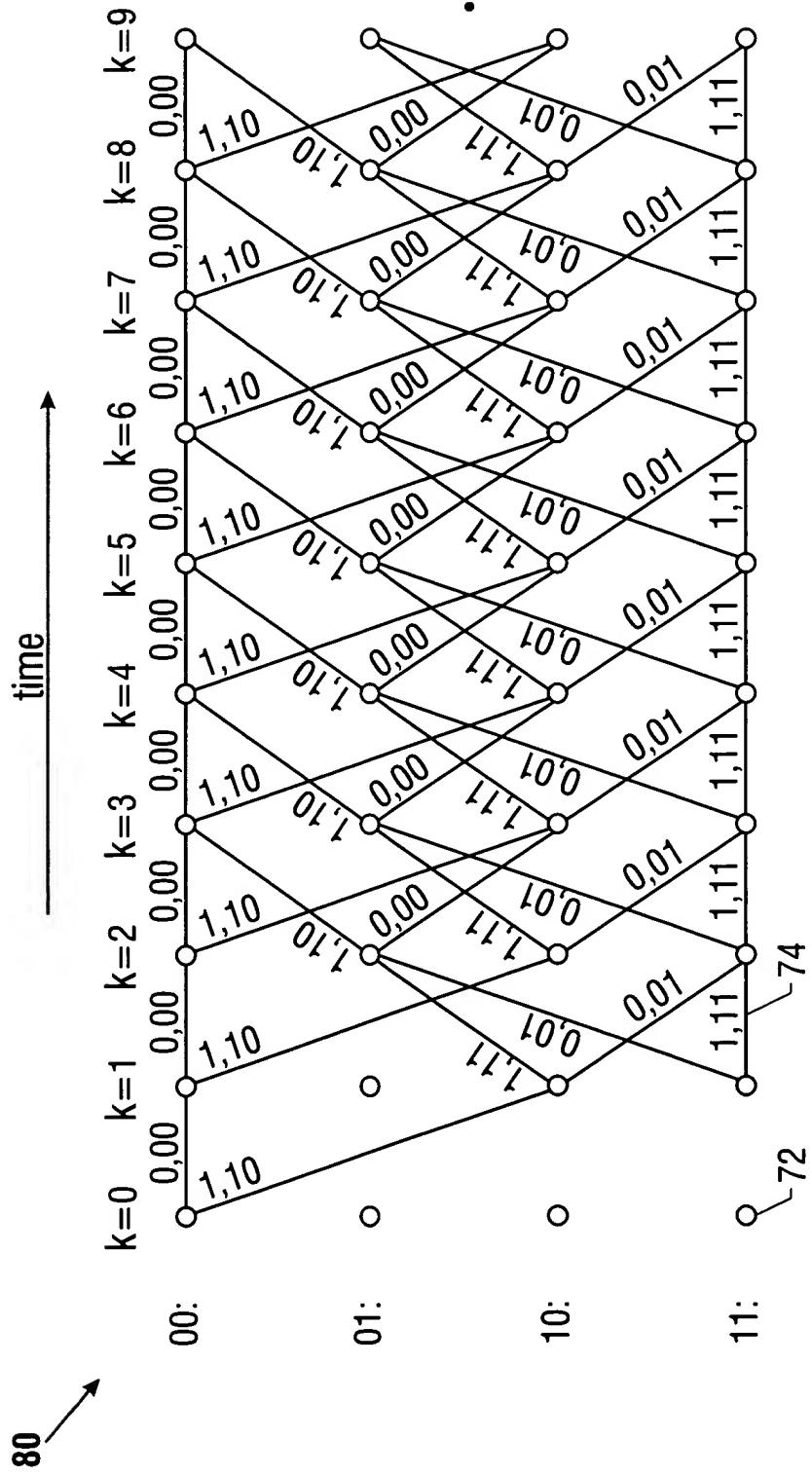


FIGURE 4

FIGURE 5



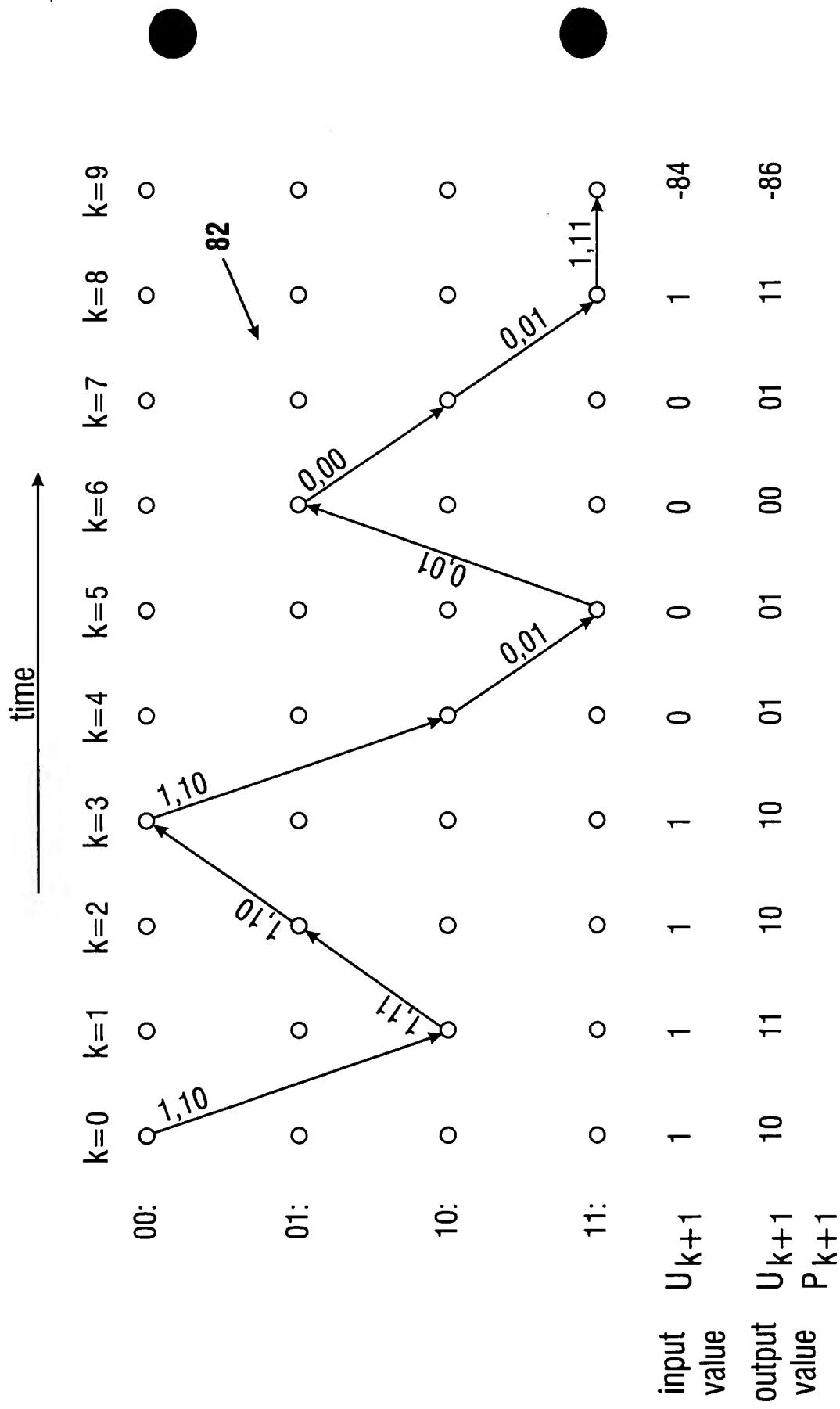


FIGURE 6

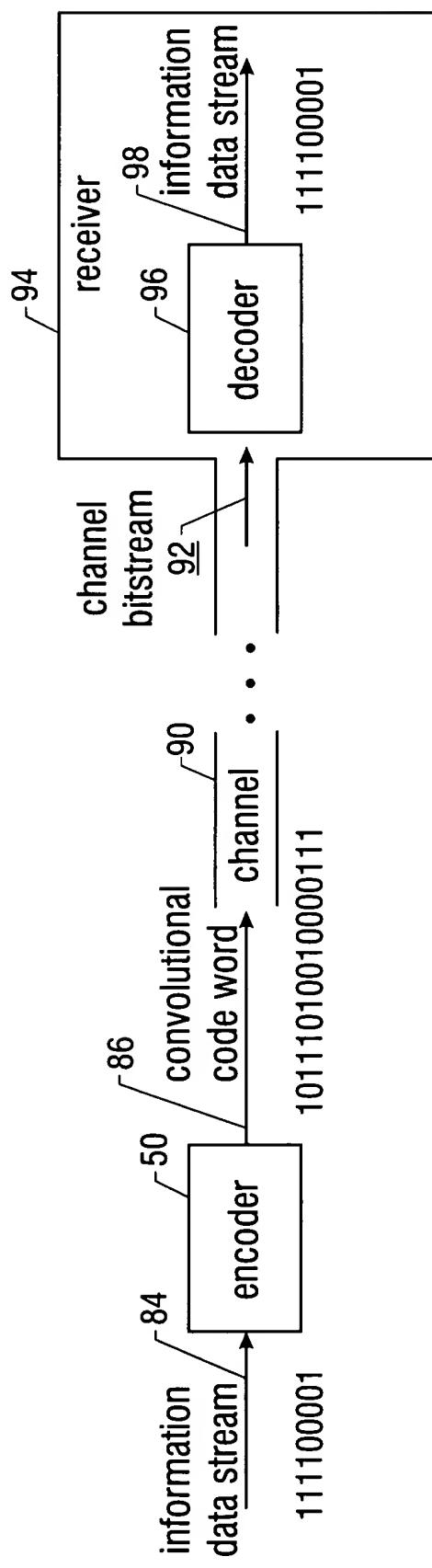
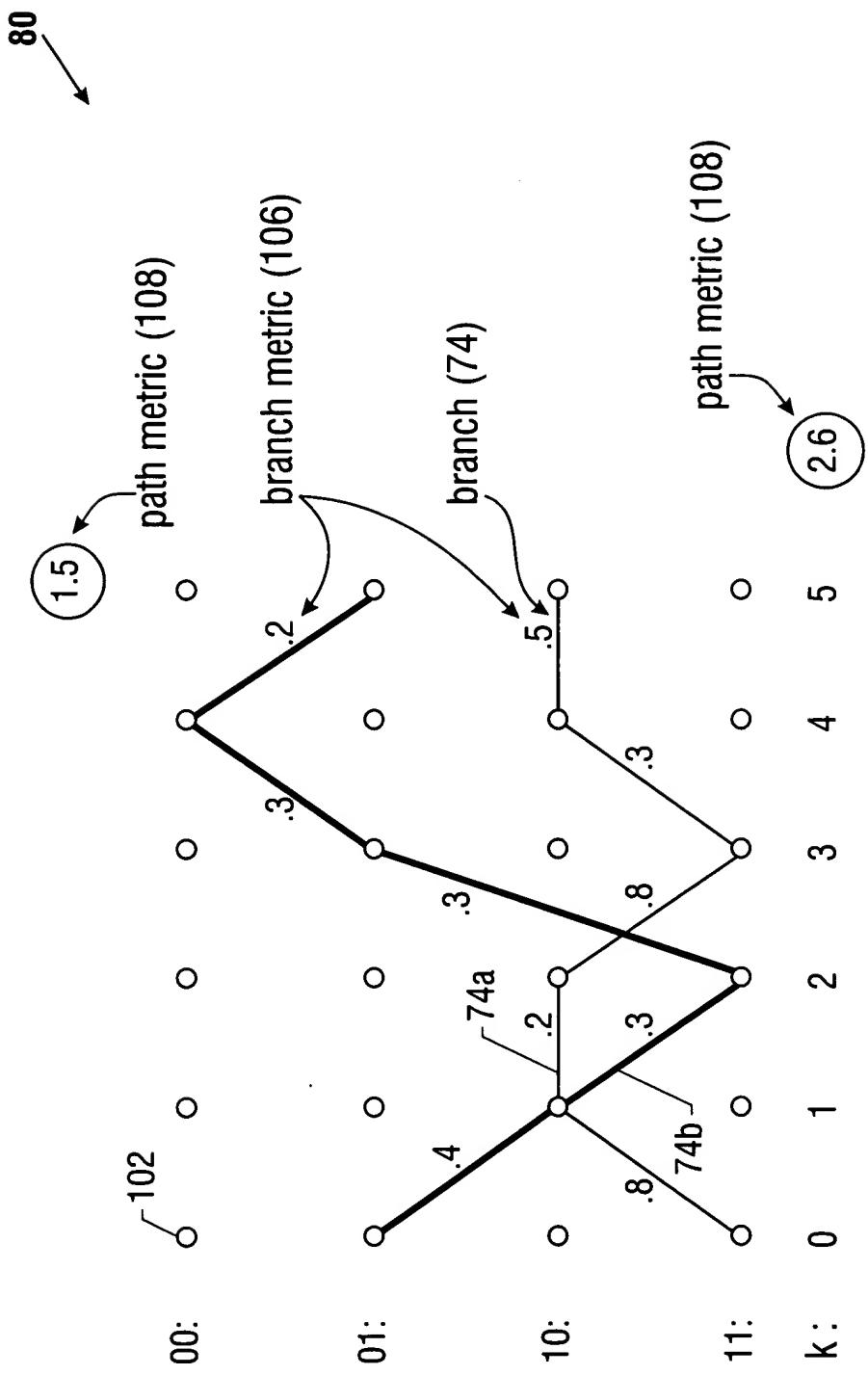


FIGURE 7

FIGURE 8



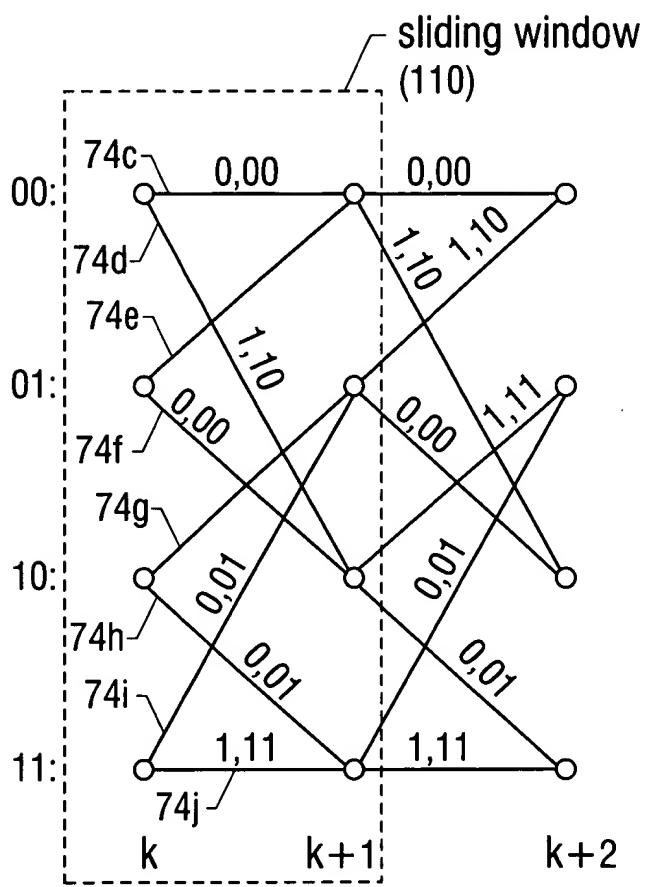


FIGURE 9

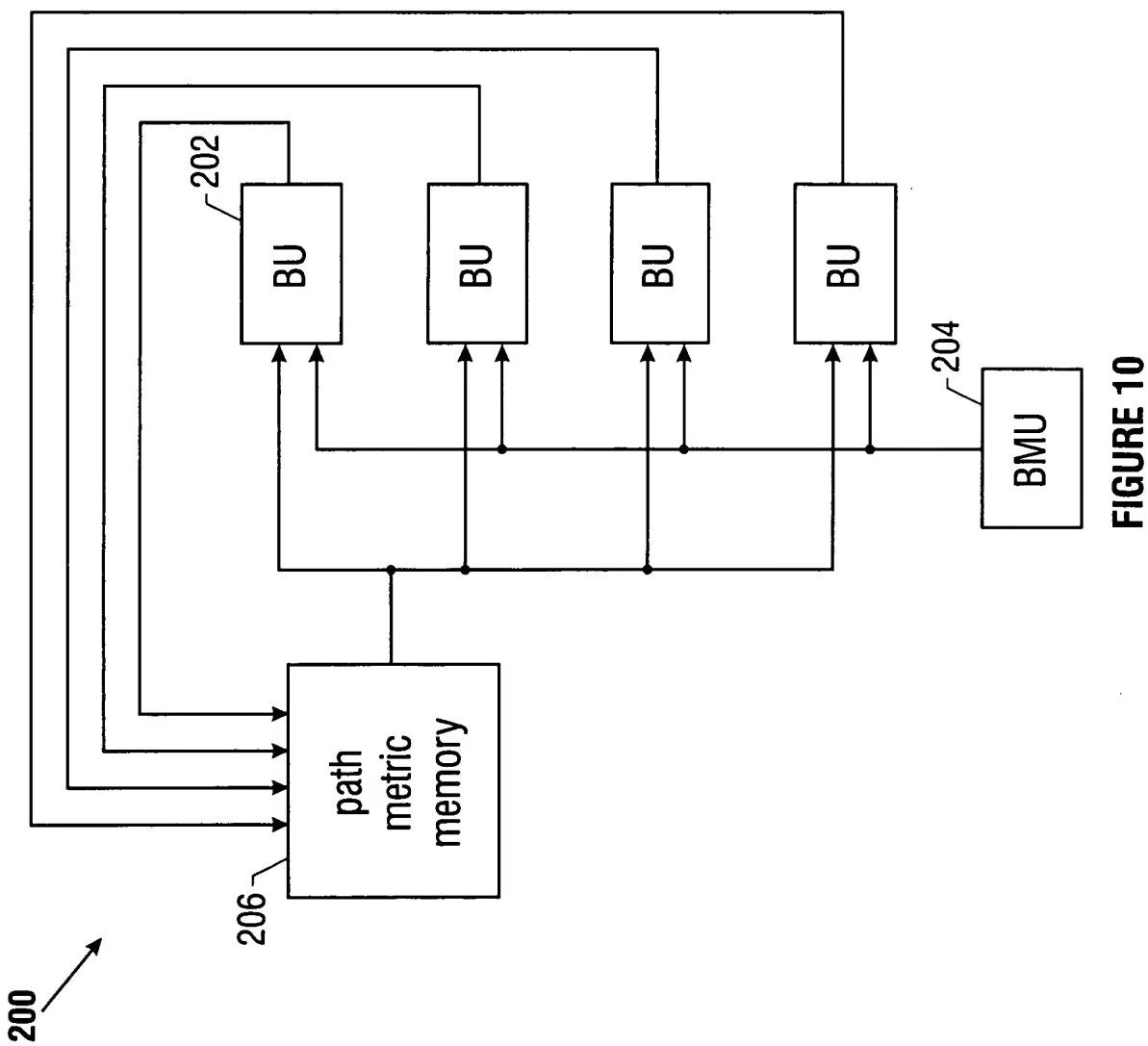


FIGURE 10

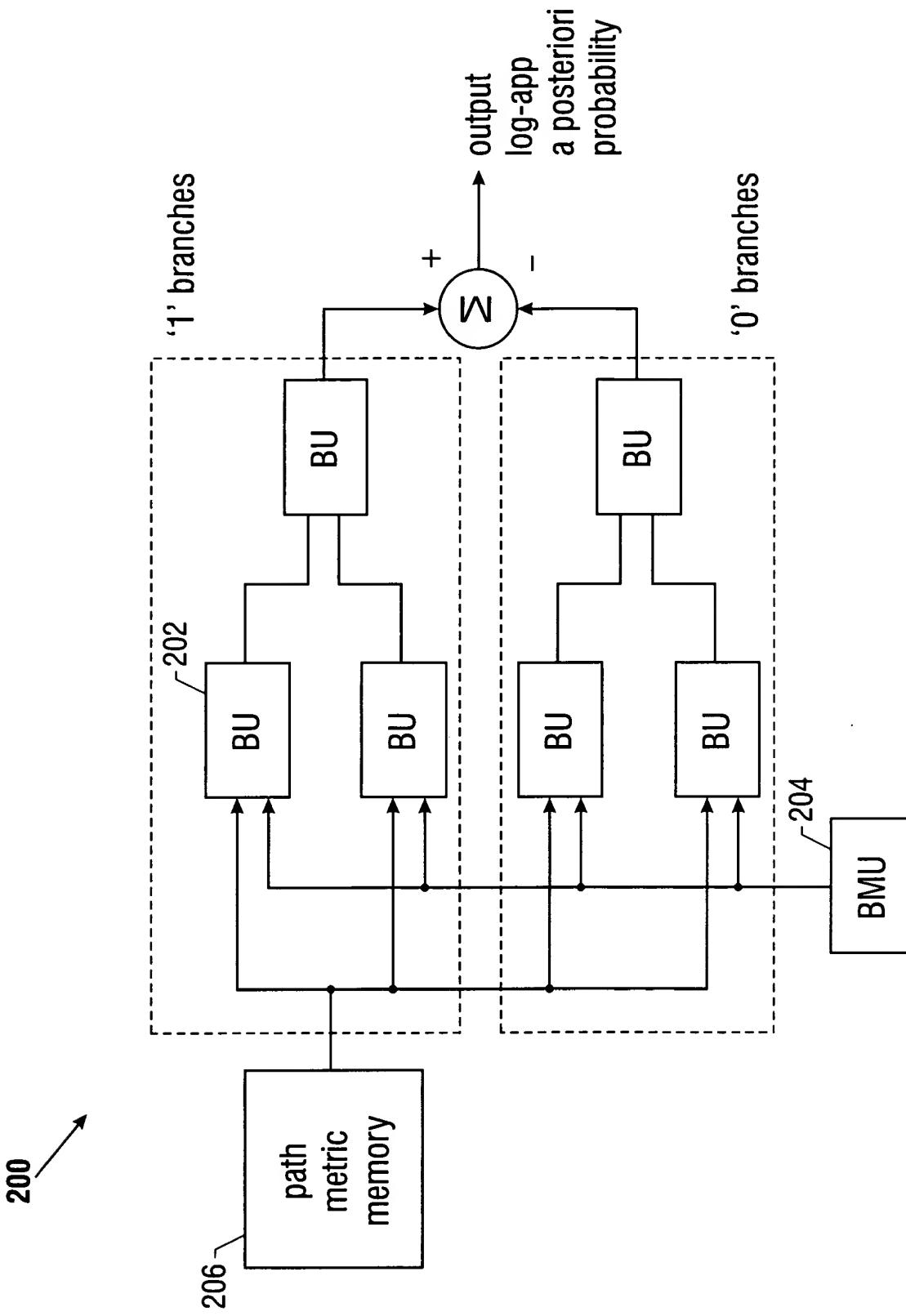
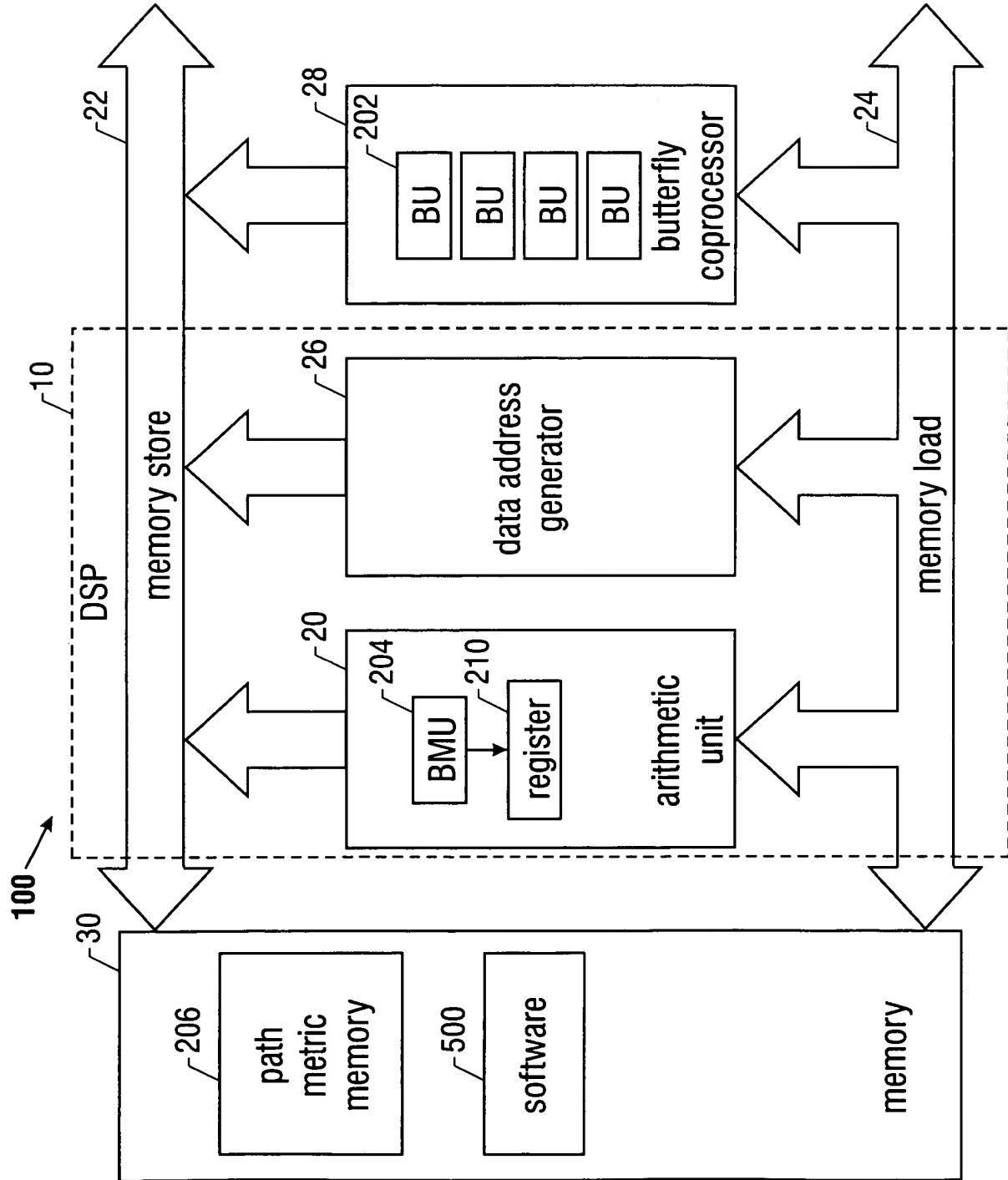


FIGURE 11

**FIGURE 12**

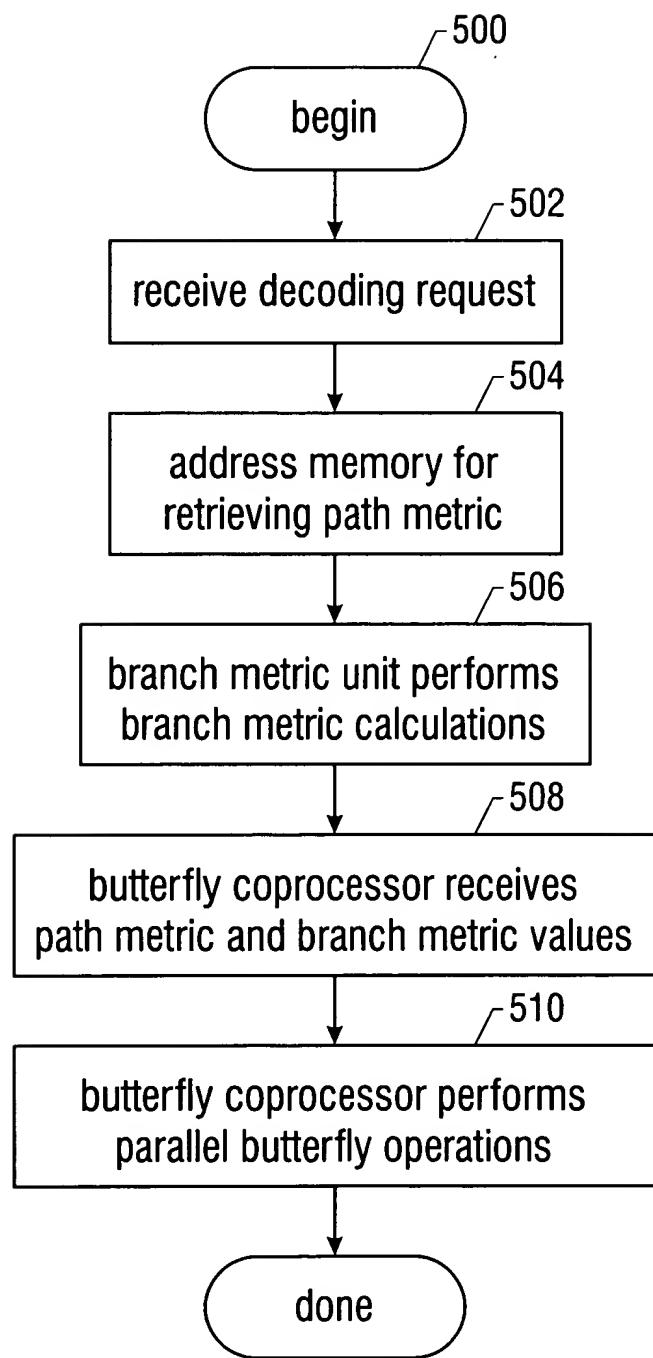


FIGURE 13

IC 10 20 30 40 50 60 70 80 90 100 110 120 130 140 150

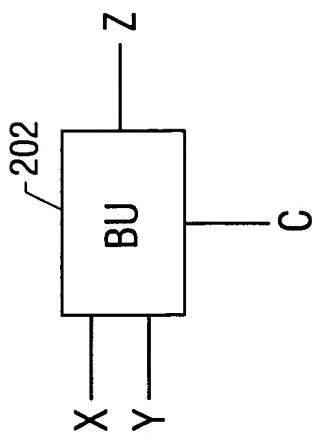


FIGURE 14A

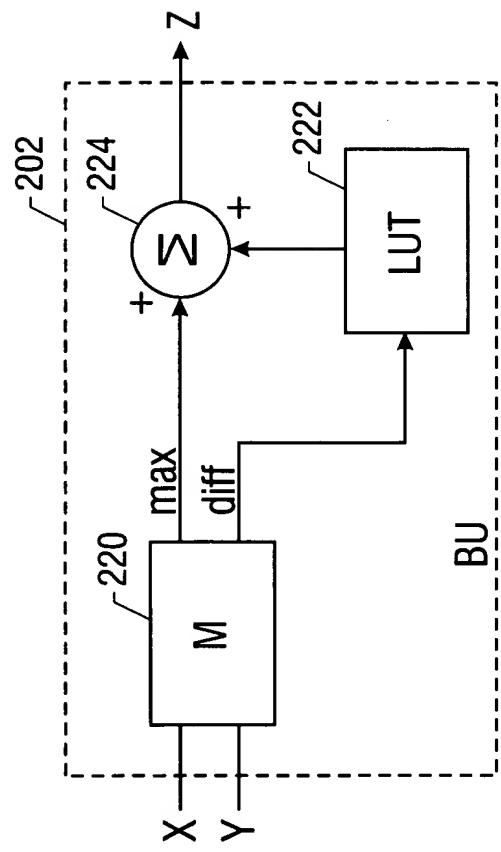
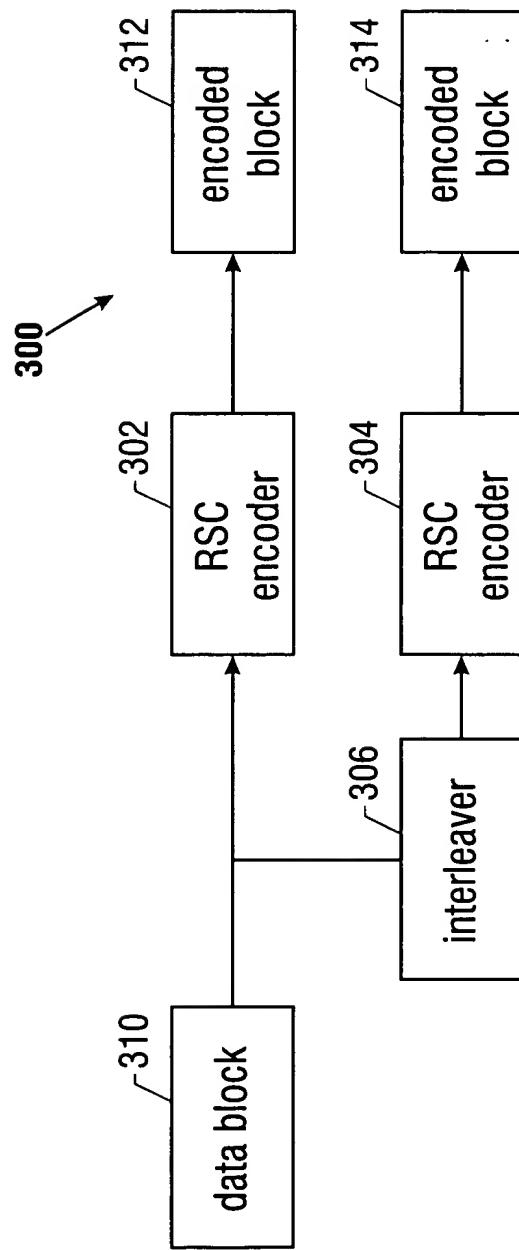


FIGURE 14B



TURBO ENCODER

FIGURE 15

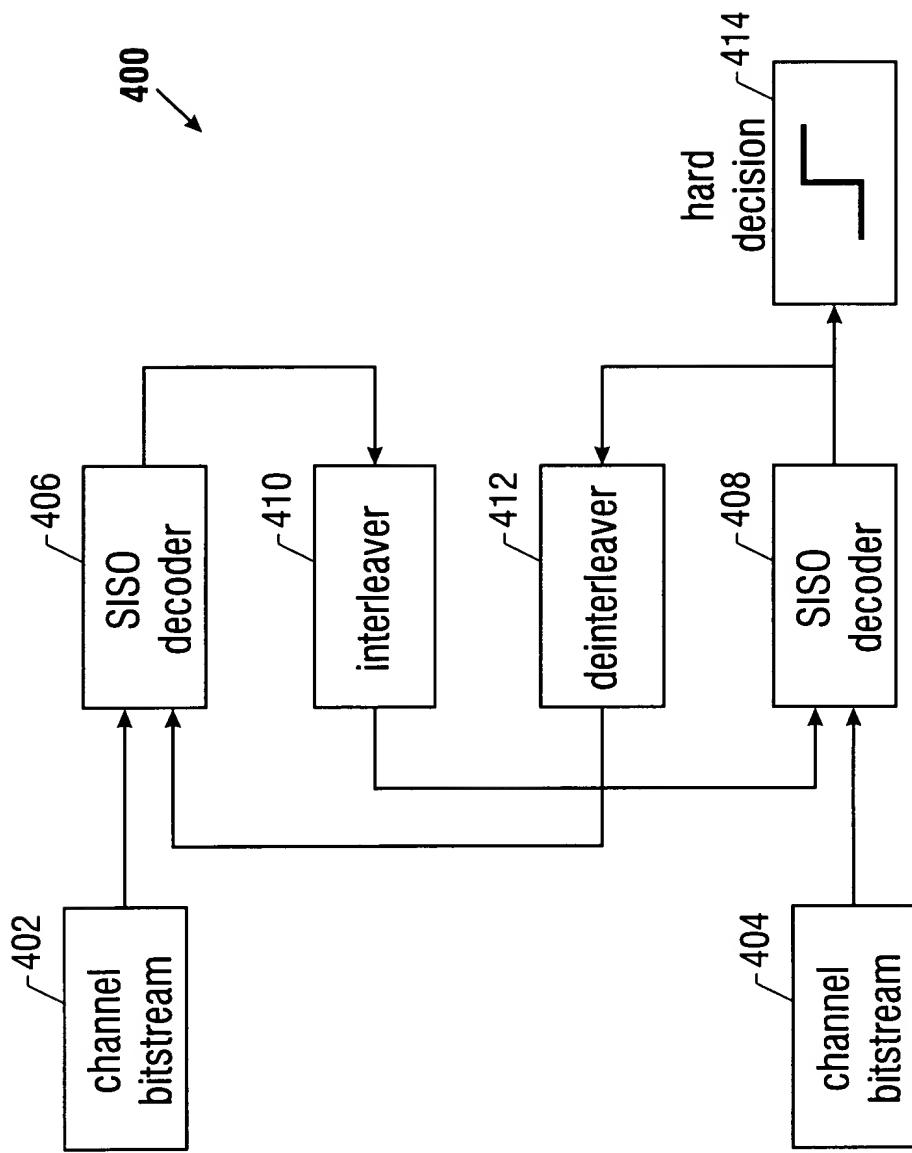


FIGURE 16